


<b>Search Notes</b>  	<b>Application/Control No.</b>  10724011	<b>Applicant(s)/Patent Under Reexamination</b>  HEER, CHRISTOPH
	<b>Examiner</b>  PETER COUGHLAN	<b>Art Unit</b>  2129

SEARCHED			
Class	Subclass	Date	Examiner
706	@pd<20021128 and 14, 47	9/25/2007	PDC
706	@pf<20021128 with 56, 57, 58	8/11/2008	PDC

SEARCH NOTES		
Search Notes	Date	Examiner
East @pd<20021128 and d flip flops, node, input, output, 'if then else', LUT, comparison, MUX, multiplexer, CLB, configurable logic blocks, look up table, logic control, register, input control node, schematic, switch, bus, comparator, event, dectector, xilinx, infineon	9/25/2007	PDC
IEEE <2003 Christoph Heer, circuit, 'if then else', 'd flip flop', LUT, look up table, CLB, configurable logic block, schematic	9/25/2007	PDC
Dogpile Christoph Heer, circuit, 'if then else', 'd flip flop', LUT, look up table, CLB, configurable logic block, schematic	9/25/2007	PDC
Inventor Christoph Heer	9/2/20075	PDC
East @pd<20021128 and configurable logic block, first input, second input, third input, fourth input, comparison, if then else, look up table, look-up table, lookup table, register, multiplexer	8/11/2008	PDC

INTERFERENCE SEARCH			
Class	Subclass	Date	Examiner
USPGPub	Independent Claims keywords .clm.	8/11/2008	PDC